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DATE MAILED: 08/26/2004

APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/787,327	02/26/2004		Austin H. Lesea.	X-1026-1D US	3981
24309	7590	08/26/2004		EXAM	INER
XILINX, IN		DTMENT	PHAM, LY D		
ATTN: LEG 2100 LOGIO		KIMENI	ART UNIT	PAPER NUMBER	
SAN JOSE,	CA 9512	24	2818		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/787,327	LESEA, AUSTIN H.					
Office Action Summary	Examiner	Art Unit					
·	Ly D Pham	2818					
The MAILING DATE of this communication							
Period for Reply	NEDI VIO OET TO EVOIDE 4	AONTHO FROM					
A SHORTENED STATUTORY PERIOD FOR R THE MAILING DATE OF THIS COMMUNICAT  - Extensions of time may be available under the provisions of 37 C after SIX (6) MONTHS from the mailing date of this communicati  - If the period for reply specified above is less than thirty (30) days  - If NO period for reply is specified above, the maximum statutory  - Failure to reply within the set or extended period for reply will, by Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ION.  FR 1.136(a). In no event, however, may a on.  , a reply within the statutory minimum of th period will apply and will expire SIX (6) MC statute, cause the application to become A	reply be timely filed irty (30) days will be considered timely. NTHS from the mailing date of this communication. NBANDONED (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on							
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• • • • • • • • • • • • • • • • • • • •	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4)⊠ Claim(s) <u>1-9</u> is/are pending in the applica	ition.						
4a) Of the above claim(s) is/are with	4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.	Claim(s) is/are allowed.						
6) Claim(s) is/are rejected.	Claim(s) is/are rejected.						
7) Claim(s) is/are objected to.	· · · <del>-</del>						
8)⊠ Claim(s) <u>1-9</u> are subject to restriction and	I/or election requirement.						
Application Papers							
9) The specification is objected to by the Exa							
	r) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to							
Replacement drawing sheet(s) including the c	•						
11) ☐ The oath or declaration is objected to by t	He Examiner. Note the attacht	ed Office Action of form F10-132.					
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for fo	oreign priority under 35 U.S.C.	§ 119(a)-(d) or (f).					
	a) All b) Some * c) None of:						
1. Certified copies of the priority docu							
2. Certified copies of the priority docu							
3. Copies of the certified copies of the	•	n received in this National Stage					
application from the International B  * See the attached detailed Office action for	• • • • • • • • • • • • • • • • • • • •	at received					
Coo the addened detailed Office detail for	a list of the continue copies no						
Attachment(s)							
1) Notice of References Cited (PTO-892)		Summary (PTO-413)					
2) Notice of Draftsperson's Patent Drawing Review (PTO-94	···/	o(s)/Mail Date Informal Patent Application (PTO-152)					
<ol> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/S Paper No(s)/Mail Date</li> </ol>	6) Other: _						

## **DETAILED ACTION**

## Election/Restrictions

1. This application contains claims directed to the following patentably distinct species of the claimed invention:

Group I, drawn to claims 1 – 4, which claim a memory device having single event upset resistant circuitry, comprising: a first inverter ..., a second inverter ..., a first transistor ..., a second transistor ..., ..., wherein each of the first and second transistors has a gate coupled to a gate bas voltage source, the gate bias voltage source putting the first and second transistors into a partially conductive state to provide the high output resistances.

**Group II**, drawn to claims 5-9, which claim a memory device having single event upset resistant circuitry, comprising: a first inverter ..., a second inverter ..., a first transistor ..., a second transistor ..., ..., wherein each of the first and second transistors has a body contact coupled to a body bias source voltage.

2. Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, no claim is generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

## Conclusion

Any inquiry concerning this communication on earlier communications from the examiner should be directed to Ly Pham, whose telephone number is **571-272-1793**. The examiner can normally be reached on Monday – Friday from 8:30am to 5:00pm, alternate Friday off. The examiner's supervisor, David Nelms, can be reached at **571-272-1787**. The fax number for the organization where this application or proceeding is assigned is 703-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Ly Pham

May 25, 2004

David Nelms
Supervisory Patent Examiner
Technology Center 2800